

Inventor(s): Janardhanan S. Ajit  
 Title: SUB-MICRON HIGH INPUT VOLTAGE  
 TOLERANT INPUT OUTPUT (I/O) CIRCUIT  
 Sheet 22 of 23  
 REPLACEMENT SHEET

FIG. 17

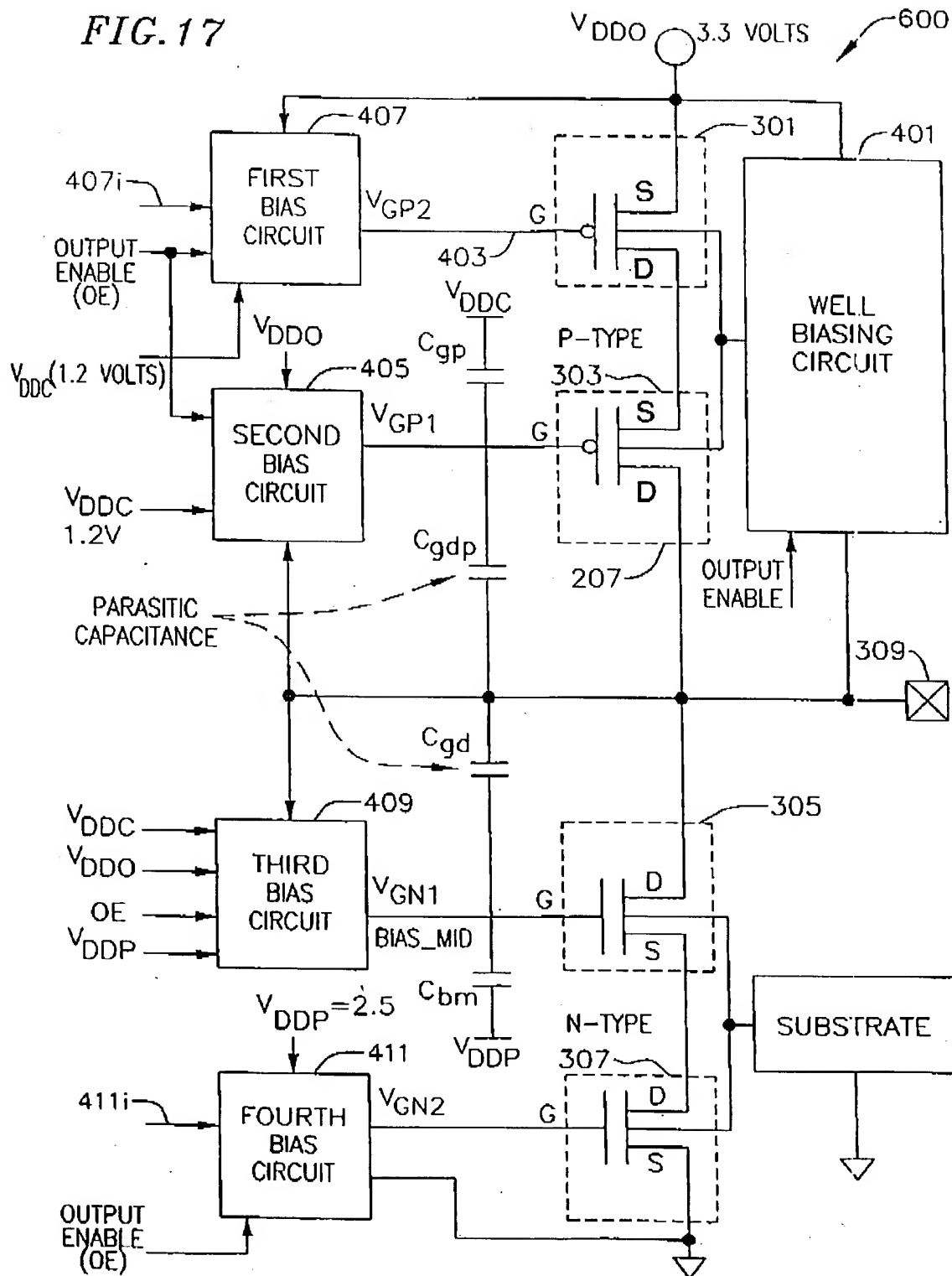


FIG. 17

